Inorganic Combinatorial Materials Science Programs in Japan

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Combinatorial Materials Science

- When Japanese scientists speak of materials property measurement, optimization or discovery, they do not use the word "combinatorial" it is automatically assumed, as there is no other viable approach for the investigation of complex materials systems.
- The Japanese Ministry of Science and Education has identified combinatorial materials methodologies as essential to the technological future of the country.



Japan Visit, February 2004

Location	Scientists Visited	Materials Class	Specific Materials	Combinatorial Synthesis Technique	Combinatorial Analysis Techniques (Primary)
Tokyo Institute of Technology ^a	Prof. Hideomi Koinuma ^b Dr. K. Itaka Prof. Yuji Matsumoto Mr. Ryota Takahashi Mr. Ken Hasegawa	Thermoelectrics	$(Ca_{1-x}A_x)_3(Co_{1-y}B_y)_4O_9 \ A=Mg, Sr, Ba, Ag, Bi \ B=Zn, Cu$	PLD ^c	PF ^d , XRD
		Organic Semiconductors	Pentacene/Al ₂ O ₃	MBE (graded thickness)	Ellipsometry
		High T _c fluxes	CuO _x –BiO _x –Bi ₄ Ti ₃ O ₁₂	PLD	RHEED
		Transparent Magnetic films	TiO ₂ :M M = transition metal dopants	PLD	SSM ^e
		Photocatalysis Sensors	Pentacene/(Sr, La)TiO ₃	PLD/MBE	AFM
NIMS	Dr. Toyohiro Chikyow Dr. K. Tatsumura Mr. T. Nagata	High-k Gate Dielectrics	HfO ₂ -Al ₂ O ₃ -Re ₂ O ₃	PLD	SMM ^f
		Epitaxial Semiconductors	GaN/ZnO/SiO ₂ /Si	PLD	

Japan Visit, February 2004

			$Sb_2Te_3 - SbBiTe_3$	MBE	PF
AIST	Dr. A. Yamamoto	Thermoelectrics ^g			
			Si-Ge	MBE	PF
		Crystalline Oxides		PLD	RHEED, XRD
University of	Prof. Mikk Lippmaa	Epitaxial Oxides			
Tokyo	Prof. Harold Hwang				
		High Tc			
		Superconductors			
		Magnetic	Epitaxial Manganate	PLD	LEED,
KEK	Dr. H. Kumigashira	Resonance	Superlattices		Synchrotron XPS ^h
		Devices			
Tohoku University	Prof. Masashi Kawasaki	Magnetic Oxides	La _{1-x} Sr _x MnO ₃ /SrTiO ₃	PLD	SSM
	Prof. A. Miyamoto				
	Mr. Tsukazaki	UV Lasers,			
	Mr. Ohtani	transparent	$ZnO/Mg_xZn_{1-x}O$	PLD	RHEED
	Dr. Yamada	semiconductors			

Roles NIST/MSEL Could Play in Inorganic Combinatorial Materials Science

- Understanding combinatorial methodology as a:
 - method of materials optimization, development and discovery
 - means of experimentally verifying computed materials properties
 - tool for determining phase diagrams and other property data



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Combinatorial Material Systems Opportunities (Inorganic)

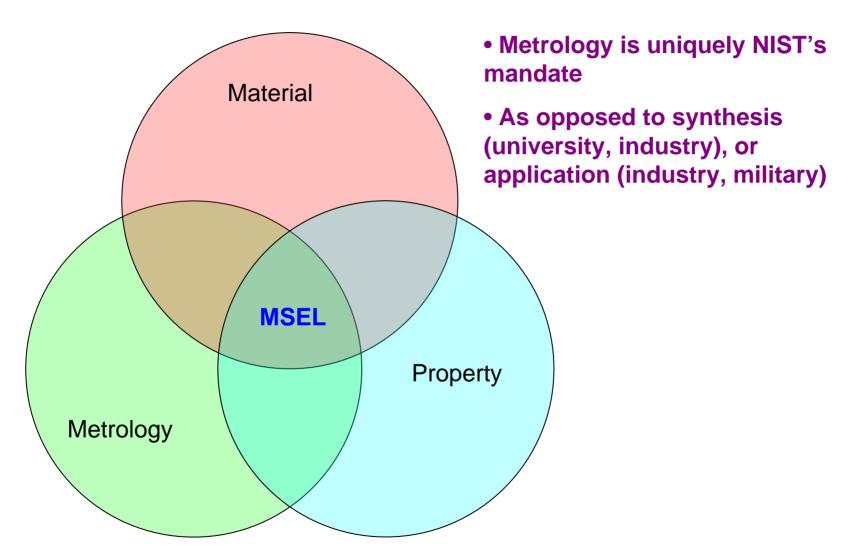
- Si Microelectronics
 - Metal gates
 - ➤ High-k gate dielectrics
 - > FeRAMS
 - Magnetic memories
- Sensor Materials

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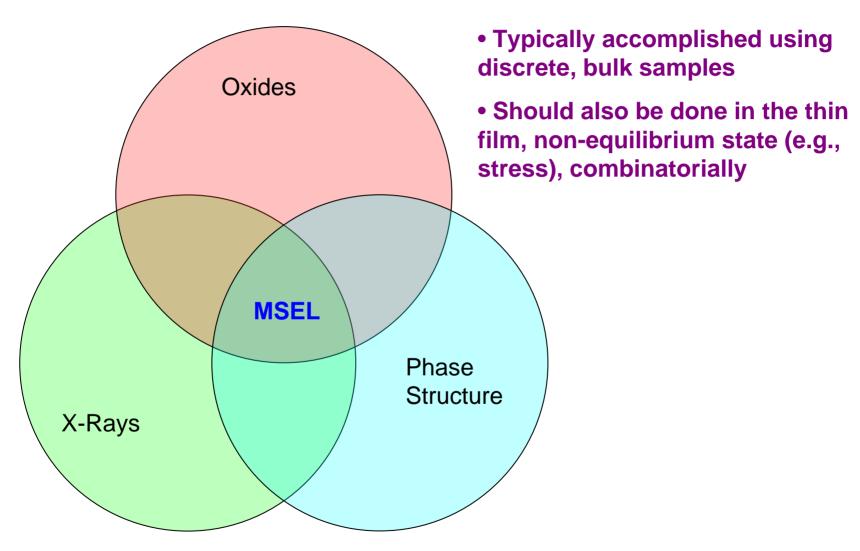
- Thermoelectric Materials
- Transparent Semiconductors
- Magnetic Semiconductors
- Magnetic Shape-memory Alloys



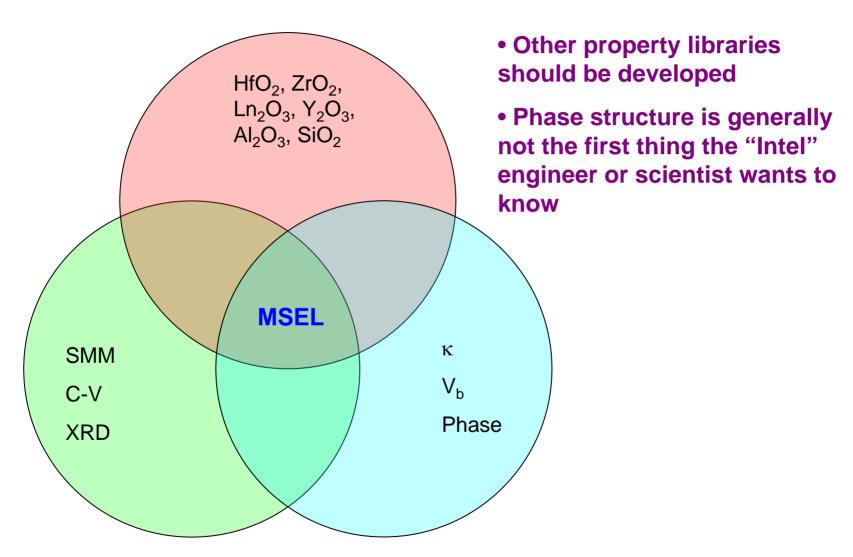
MSEL: Role of Combinatorial Methodology



Phase Diagram: Typical NIST Product



Alternate Gate Dielectrics



Advanced Gate Stacks

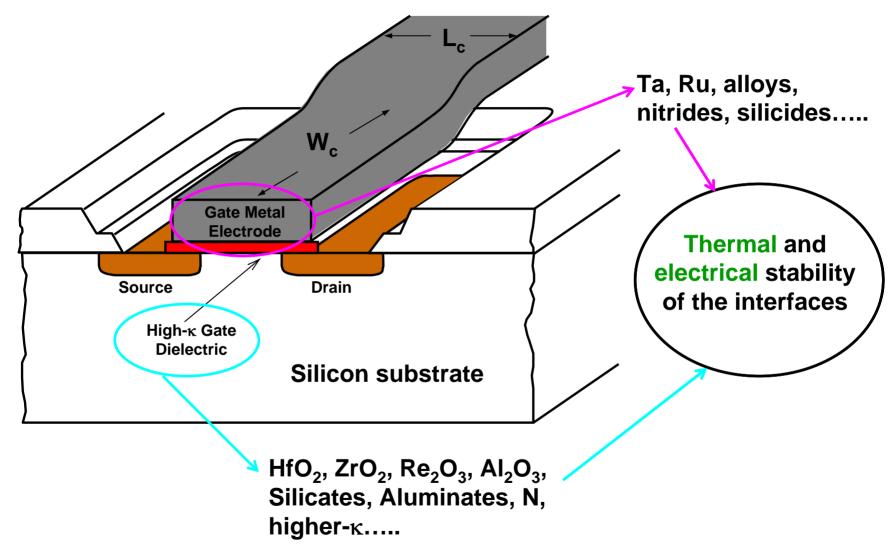
The 2003 ITRS states:

"We have entered the era of *materials limited* device scaling...The challenges associated with these *diverse new materials*, and the control of the *physical interfaces* associated with these materials, constitutes the central theme of the front-end processing challenges... In no area is this issue *more clear or urgent than in the MOSFET gate stack*".

 The advanced gate stack is a grand materials science and engineering challenge for International Sematech



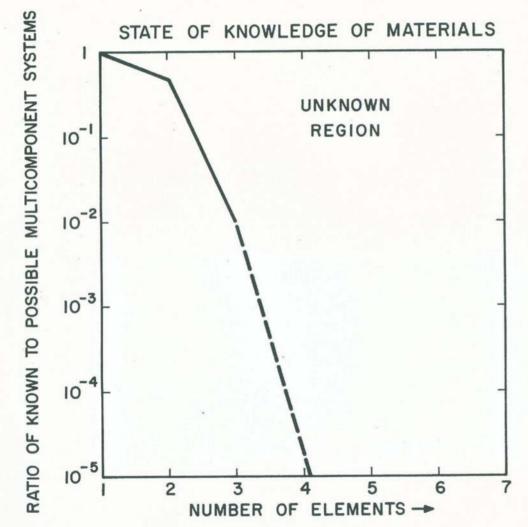
Complexity of the Advanced Gate Stack





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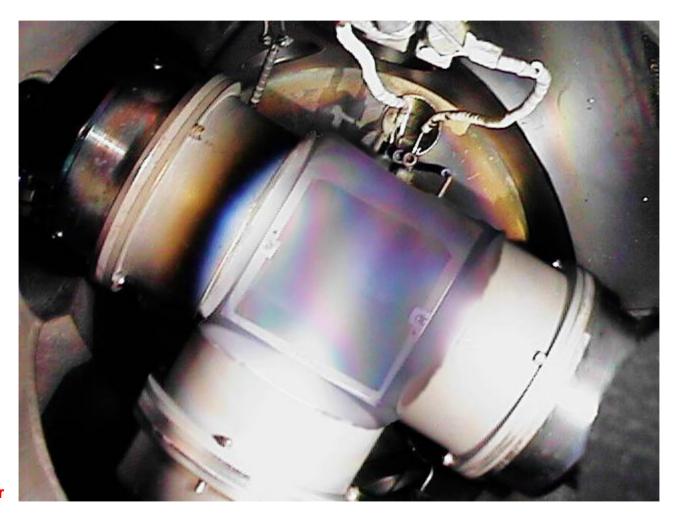
Combinatorial Materials Science



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Whatever one is trying to make, the choice of materials is often bewildering. Novel combinatorial approaches allow one to reduce the time and costs necessary to optimize results.

Off-Axis Reactive Sputtering of High-k Gate Dielectrics



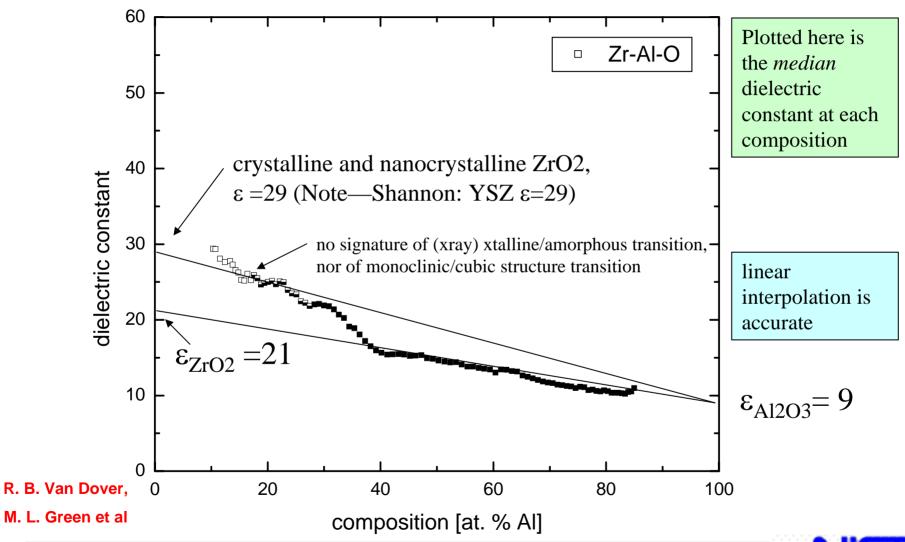
R. B. Van Dover

"But that's not the way we'll make it..."

- Combinatorial methodology excels for:
 - Observation of trends ("slope" is more important than exact value of a property)
 - Screening, i.e., finding the "sweet spot" of the response surface
 - Measurement of structure-insensitive properties
- Results must be benchmarked and carefully interpreted

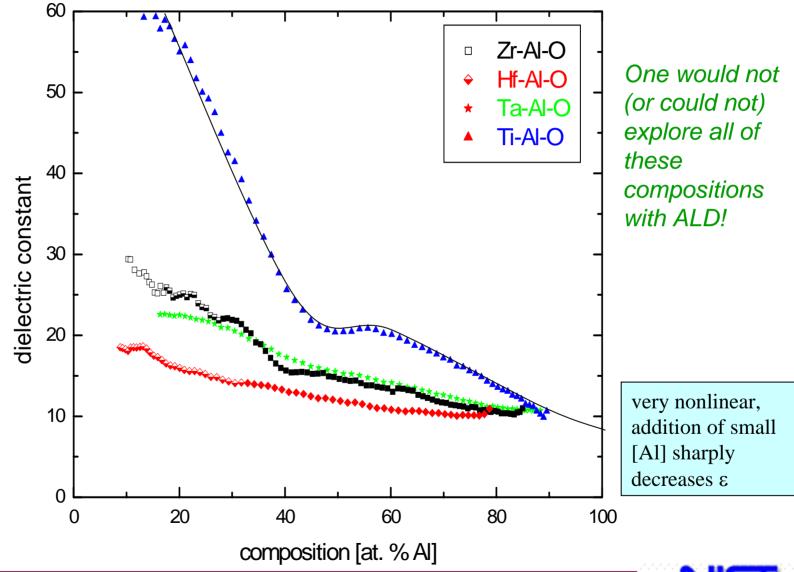


Zr-Al-O Dielectric Constant vs. Composition



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Aluminates: Dielectric Constants



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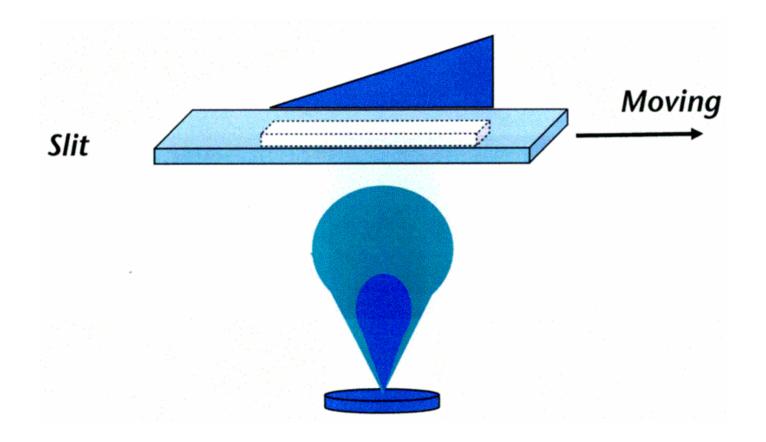
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Design of Experiment

- Typical libraries:
 - Ta-Ru-Si on Hf-O-N
 - Ta-Si-N on high-κ (newly identified)
- What information (minimum) must we know about the advanced gate stack?
- thermal stability of the gate dielectric and metal gate layers and their interfaces (crystallization, interdiffusion, interfacial reactions....)
- **electrical characteristics** of the stack (V_T , Φ_b , Q_f , Q_{it}), and how are these properties affected by thermal treatment?
- What measurements will be made to characterize the gate stack?
 - nanocalorimetry
 - micro x-ray diffraction, electron backscatter diffraction
 - C-V curves
 - i-V curves
 - conductance as a function of frequency
 - gate metal work function, $\Phi_{\rm m}$

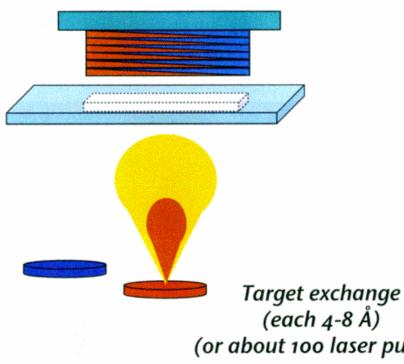


Compositional Gradient: Pulsed Laser Deposition (PLD)



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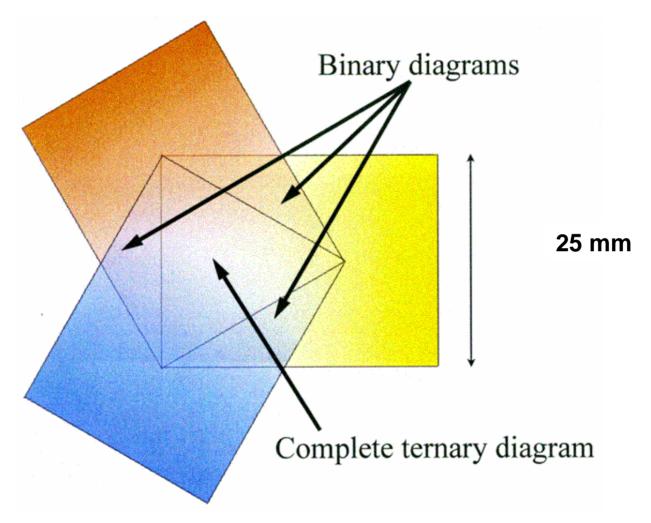
Combinatorial Deposition (2 materials)



Annealing results in uniform graded layer of any thickness

(or about 100 laser pulses)

Ternary System



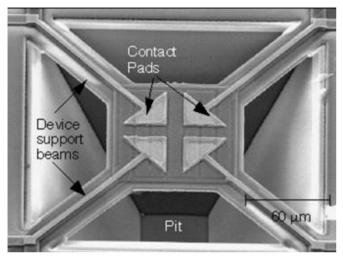


Combinatorial Analysis Techniques: Advanced Gate Stacks

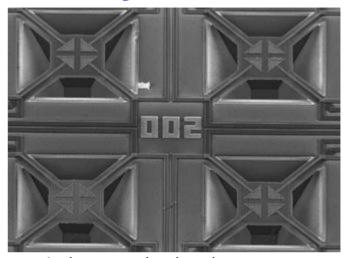
Quantity or Property	Measurement	Technique	Comments
Phase change	Differential temperature	Nanocalorimetry	
Crystallinity	Diffracted intensity	Electron backscatter diffraction	
Crystal structure	Diffracted intensity	Micro x-ray diffraction	
Threshold voltage	Flatband voltage	C-V curves	
Barrier height for tunneling	Current	i-V curves	
Fixed Charge	Conductance	Conductance = $f(\omega)$	
Interfacial States	Metal work function	Kelvin probe	Knowing V_{fb} and Φ_{s}



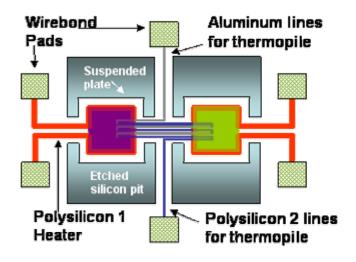
Nanocalorimetry



single microhotplate



4-element microhotplate array for gas microsensor prototypes



microcalorimeter schematic